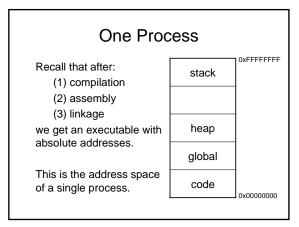
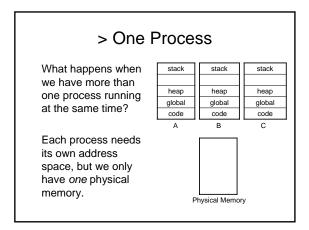
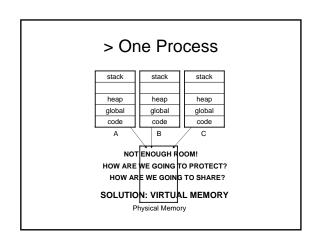
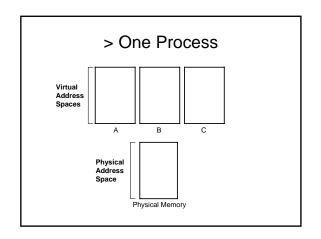
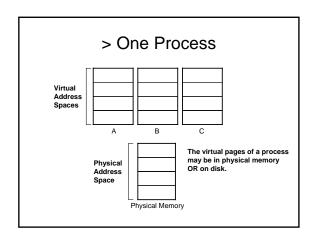
Virtual Memory Review

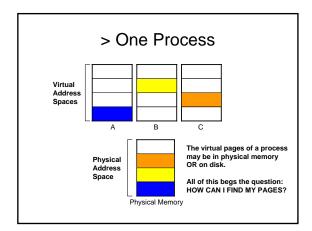


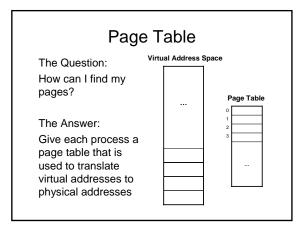


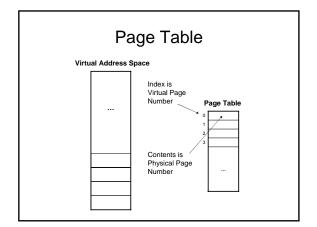


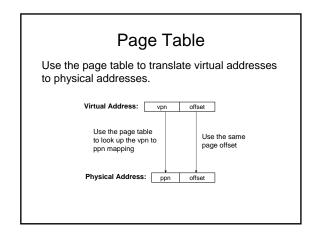












Page Table What other information could we store in the page table? Valid bit (required) Physical page number (required) Dirty bit (required) Referenced bit Access rights Why don't we store the virtual page number? What are the other bits used for? Why is the dirty bit required?

Page Table Why don't we store the virtual page number? The vpn is the index for the page table. What are the other bits used for? Valid → 1 if in memory, 0 if on disk Dirty → has the page been written to? Reference → used to approximate LRU Access Rights → read only, etc. Why is the dirty bit required? Memory is always write back, so upon eviction we need to know whether to write the page back to disk.

Ex1

What does it mean when the valid bit is zero?

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The page is on disk.

Ex2

Virtual addresses: 40 bits Physical addresses: 30 bits

Page size: 8 KB

How many bits are used for the page offset, virtual page number, and physical page

number?

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Virtual addresses: 40 bits Physical addresses: 30 bits

Page size: 8 KB

How many bits are used for the page offset, virtual page number, and physical page

number?

offset bits: 13, vpn bits: 27, ppn bits: 17

Ex3

Virtual addresses: 40 bits Physical addresses: 30 bits

Page size: 8 KB

How many page table entries are required for a single process?

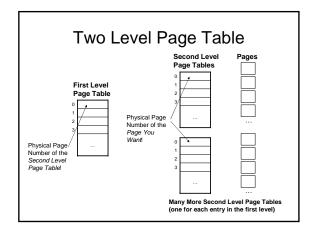
Ex3

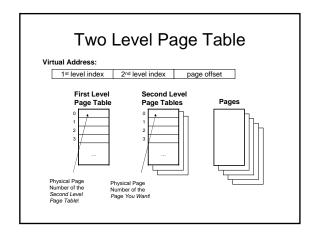
Virtual addresses: 40 bits Physical addresses: 30 bits

Page size: 8 KB

How many page table entries are required for a single process?

2²⁷. Page Tables are too big, so use two level page tables!





Ex4

The two level page table will have more entries than if we had only used a one level page table. How exactly are we saving space?

Ex4

The two level page table will have more entries than if we had only used a one level page table. How exactly are we saving space?

In the one level page table, the page table had to stay in memory all the time. In the two level page table, only the first level page table needs to stay in memory all the time... huge savings!

Ex5

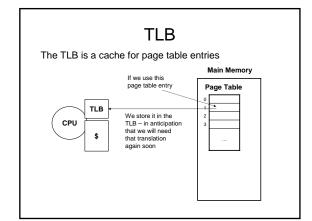
Assuming our data is in main memory, how many times do we need to go to memory in order to do one memory access (assuming that all you have is a two level page table)?

Ex5

Assuming our data is in main memory, how many times do we need to go to memory in order to do one memory access (assuming that all you have is a two level page table)?

3. One for each level of the page table and one for the data itself.

This is too slow, so use a cache (TLB) to keep recently used page table entries.



Ex6

What information should we store in the TLB?

Ex6

What information should we store in the TLB?

Everything we stored in the page table entry (valid, ppn, dirty, referenced, access rights) AND the vpn.

bits	vpn	ppn

Ex7

The TLB is generally much smaller than the data cache, but the TLB will certainly have a higher hit rate than the data cache. Why?

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The TLB is generally much smaller than the data cache, but the TLB will certainly have a higher hit rate than the data cache. Why?

Memory accesses in the same page will use the same TLB entry.

Memory accesses in the same block will use the same cache entry.

Pages: 1KB-8KB. Blocks: 32B-64B.

Ex8

How (the heck) are page tables, the TLB, the data cache, main memory, and disk all related?

See lecture notes from 8/4.